IN THE SPECIFICATION:

Please replace the **BRIEF DESCRIPTION OF THE DRAWINGS** (excepting the header itself) with the following amended paragraph:

The teachings of the present invention can be readily understood by considering the following detailed description in conjunction with the accompanying drawing in which:

FIGURE 1 is a block diagram of a computer system consistent with the invention;

FIGURE 2 is a block diagram of an exemplary software environment for the computer system of FIGURE 1;

~~FIGURES 3A, 3B and 4-8 depict code segments useful in understanding the invention;~~

FIGURE 3A depicts a code segment illustrating a direct call of a subroutine;

FIGURE 3B depicts a code segment illustrating an indirect call of a subroutine;

FIGURE 4 depicts a code segment 400 illustrating an inlined access of a subroutine;

FIGURE 5 depicts a code segment illustrating an optimization enabled by the inlining technique of FIGURE 4;

FIGURE 6 depicts a code segment illustrating the use of multiple copies of statically stored objects;

FIGURE 7 depicts a code segment useful in understanding the present invention, and more particularly illustrates a problem that can occur with static storage if multiple copies of a class attempt to share the same static;

FIGURE 8A depicts a code segment and address space utilization useful in understanding the present invention;

FIGURE 8B depicts a reentrant static addressing scheme utilizing the code segment of FIGURE 8A;

FIGURE 9 depicts a data structure representing a Java® class file;

FIGURE 10 depicts a data structure representing a loaded Java® class;

FIGURE 11 depicts a data structure representing a compiled Java® class;

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FIGURES 12 and 13 depict code segments useful in understanding the invention;

FIGURE 14 depicts a graphical representation useful in understanding the invention;

FIGURE 15 depicts a plurality of data structures illustrating constant pool entries and constant resolution entries;

FIGURES 16A and 16B (collectively referred to as FIGURE 16) depict[[s]] a flow diagram of a constant resolution process;

FIGURE 17 depicts a graphical representation useful in understanding the invention;

FIGURE 18 depicts a flow diagram of a process according to the invention; and

FIGURE 19 depicts a flow diagram of a method for compiling an externally resolved subroutine according to an embodiment of the invention.

To facilitate understanding, identical reference numerals have been used, where possible, to designate identical elements that are common to the figures.

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